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FILED PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of:**

Connell et al.

**Serial No.:** 10/082,372

**Filed:** February 25, 2002

**For:** WAFER BACK SIDE COATING TO  
BALANCE STRESS FROM PASSIVATION  
LAYER ON FRONT OF WAFER AND BE  
USED AS A DIE ATTACH ADHESIVE

**Examiner:** Unknown

**Group Art Unit:** 2812

**Attorney Docket No.:** 5083US (01-0428)

CERTIFICATE OF MAILING

I hereby certify that this paper or fee along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

May 21, 2002  
Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

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REQUEST FOR CORRECTED FILING RECEIPT

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Sir:

Attached is a copy of the official filing receipt received from the PTO in the above application. Issuance of a corrected filing receipt in accordance with the annotations on the attached filing receipt copy is respectfully requested.

The errors to be corrected are as follows:

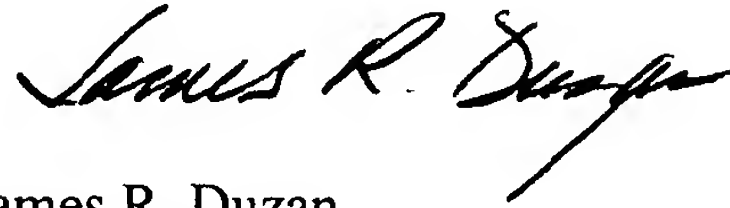
**IN THE TITLE:**

After "AS" insert --A--.

Serial No. 10/082,372

These corrections are not due to any errors by applicants and no fee is due.

Respectfully submitted,

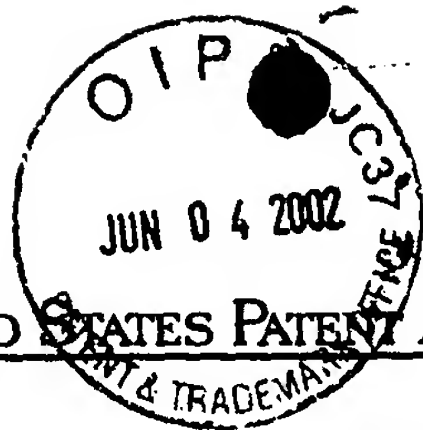


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Date: May 21, 2002

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/082,372	02/25/2002	2812	2096	2269-5083US	5	86	5

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**Applicant(s)**

Michael E. Connell, Boise, ID;  
Tongbi Jiang, Boise, ID;

**Domestic Priority data as claimed by applicant**

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**Foreign Applications****If Required, Foreign Filing License Granted 03/22/2002****Projected Publication Date: 08/28/2003****Non-Publication Request: No****Early Publication Request: No****Title**

Wafer back side coating to balance stress from passivation layer on front of wafer and be used as die attach adhesive **A**

**Preliminary Class**

438

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